



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Attn: OIPE

Makoto KUDO

Application No.: 10/804,004

Docket No.: 118768

Filed: March 19, 2004

For: SEMICONDUCTOR DEVICE, SEMICONDUCTOR CIRCUIT, ELECTRONIC
EQUIPMENT, AND METHOD OF CONTROLLING CLOCK-SUPPLY

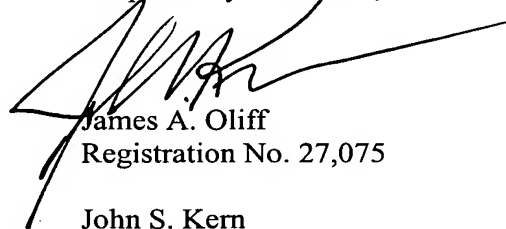
REQUEST FOR CORRECTION OF PALM RECORDS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records. A Supplemental Application Data Sheet is attached.

Respectfully submitted,



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Date: July 28, 2004

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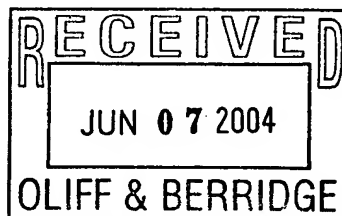


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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/804,004	03/19/2004	2811	770	118768	8	11	3

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CONFIRMATION NO. 6984

FILING RECEIPT



OC000000012874887

Date Mailed: 06/04/2004

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Assignment For Published Patent Application
 SEIKO EPSON CORPORATION, Tokyo, JAPAN;

Domestic Priority data as claimed by applicant

Foreign Applications
 JAPAN 2003-078089 03/20/2003

If Required, Foreign Filing License Granted: 06/04/2004

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

Title

Semiconductor device, semiconductor circuit, electronic equipment, and method of controlling clock-supply

Preliminary Class

257

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